



From what I gather the DX synths are actually phase distortion. A table with a sine being read out for a specific phase.

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Here's something that was posted to the music-dsp list a few years ago. It has to do with the OPL2/OPL3 chips that were used in PC soundcards, but it's probably the same techniques that are involved for all of Yamaha's chips. <u>http://music.columbia.edu/pipermail/...2008-</u> April.txt

And here Adlib / OPL2 / YM3812

...

Date: Fri Apr 18 02:44:09 2008 Subject: [music-dsp] YM3812 reverse engineering

Me and Matthew Gambrell have an interest in emulation of YM3812, which is the OPL2 sound chip found in the Adlib and 8-bit Sound Blaster cards. A later derivative OPL3 is found in early 16-bit Sound Blasters. We sent one YM3812 and one YMF262 (OPL3) to MEFAS for decapsulation; the cost was around 90 USD each. They indicated that the chips would still be operational after decapsulation, but we had no need to test this. Looking at the revealed YM3812 die surface with a microscope turned out two ROM's.

## [snip]

The contents could be read bit-by-bit. The first ROM was a log-sin waveform table, containing one quarter of a sine wave, 256 samples long. The second ROM was an exponential table, 256 samples long. There were no other ROM's larger than 16 samples. This is strong evidence that YM3812 produces the sound without any multiplications, using for frequency modulated (actually phase modulated) synthesis the formula:

out = exp(logsin(phase2 + exp(logsin(phase1) + gain1)) + gain2)

[snip]"

For more professional parts, the kinds used in the synths, you might expect to see variations on this theme, perhaps longer tables or wider data words; apparently the OPL chips have 11-bit resolution per operator, I think most synths after the first DX7s have higher resolution than that.





Originally Posted by **EvilDragon** *One DAC for all voices.* 

...but each voice is converted individually.

Quote:

Originally Posted by **vinyl\_junkie** D There were no DSP's in 1983 or were very expensive.

The main feature of a DSP is a fast hardware Multiplier. 16x16 multipliers were available already in the mid 70s, but were very expensive because they require a lot of logic gates. The Yamaha FM synths instead use logarithms; a multiply is replaced by an addition and exponential table lookup. Many of the implementation details are described in <u>patent 4554857</u>.

Quote:

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It's phase modulation; something is added to the carrier's phase accumulator, while phase distortion modifies it with a piecewise linear function.

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The GS-1/GS-2, OPL series chips and OPP (DX100, FB01, etc.) have 256 entry tables, meaning that the complete sine wave is 1024 samples long. Everything else (CE20/25, TX81z, DX7, DX7II, SY77, FS1R, etc.) uses 1024 entry tables, so the complete sine wave is 4096 samples long.

Lower table resolution leads to added high harmonics (often aliased), noise and rough sounding envelopes (zipper noise). The additional waveforms that are present in some models are generated by reading out the same quarter log sine table in different ways.

DACs, sample rates, etc. also vary between models. DACs add significant distortion in the GS-1/2, CE20/25,  $\underline{DX7}$  and 4 operator models, but are relatively insignificant in the others.

Quote:

Originally Posted by **kjukambe** 🖾 Thanks for the info! And what do you think about DX7s difference with

FM7/8 (as well as other recent hardware and software implementations) likely use smooth interpolation to generate the sines, which eliminates the effects of the low resolution tables. DACs aren't emulated. The envelopes and modulation aren't identical, so patch conversion is imperfect (and thus direct A/B comparisons are somewhat flawed). It's FM as it's mathematically supposed to be, but it leaves out the high harmonics, noise and distortion that people have come to associate with the Yamaha synths.











externally available signals that can drive sample and holds.









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