

PROCESSOR TYPE PDP-11

Phase Encoded
Formatter Control

TR07F-B0001 CODE: DF ML: A WL: A

OCT-73 - PROBLEM 1: Spacing does not stop at EOF .

CORRECTION 1: Add a gate to stop FSPG and set FRDY when FEDF is set.

PROBLEM 2: Rewind flip-flop does not always set.

CORRECTION 2: Change set signal to **FSFN H** .

PROBLEM 3: Logic does not read a record with an odd number of bytes.

CORRECTION 3: Allow final RS to transfer the odd byte to memory.

PROBLEM 4: Prints and Wire List do not match.

CORRECTION 4: Update Wire List and print set accordingly.

PROBLEM 5: Address bits 16 and 17 missing from bus.

CORRECTION 5: Add address bits 16 and 17 to bus.

In-plant effectivity -Retrofit immediately

Field effectivity -Retrofit all TR07-F's

(Time To Install And Test 3.0 Hours.) (Kit Contents -F1061 -FCO/Prints And Parts)